



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO et al.**

Art Unit: **2814**

Application Number: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation Number: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE
THRESHOLD CHARACTERISTIC**

Attorney Docket Number: **020244**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop: Response

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

March 26, 2008

Sir:

This paper is filed in response to the Office Action dated October 26, 2007. The response date having been extended by two months to March 26, 2008 by the Petition for Extension of Time filed herewith.

Remarks/Arguments begin on page 2 of this paper.